

Sierra Components, Inc.

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		PAD FUNCTION
140 mils	SOURCE GATE	
	CHIP BACK IS DRAIN	
The information given is believed to be correct at the time of issue. Please verify your requirements prior to commencement of any assembly process, as no liability for omission or error can be accepted. Chip back potential is the level at which bulk silicon is maintained either by bond pad connection or in some cases the potential to which the chip back must be connected if stated above. Note: 1 mil = 0.001inch		
APPROVED		DIE INFORMATION
D Markham	IRFC5305BV	DIMENSIONS (Mils): 110 x 140 x 12 BOND PADS (Mils): GATE = 20 x 25 MASK. REF:
DATE: 27/03/03	INTERNATIONAL RECTIFIER	GEOMETRY: BACK POTENTIAL: DRAIN METALLISATION
DG 10.1.2 Rev B, 7/19/02		TOP: Al BACK: CrNiAg